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The image shows a Lake Shore Measure Ready 155 Precision I/V Source. It is a compact, silver-colored electronic device with a large color LCD screen on the left side. The screen displays '10.0000 mV' in large digits, with 'Frequency 100.000 kHz' and 'DC Offset 0.0000 mV' below it. To the right of the screen are several control buttons and a set of four colored terminals (red, green, blue, and black) for connecting test leads. The Lake Shore logo is visible on the top left of the device's face.

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Direct current heating in superconductor–insulator–superconductor tunnel devices for THz mixing applications

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DC heating effects in superconductor–insulator–superconductor (SIS) tunnel junctions are studied by comparing junctions sandwiched between niobium or aluminum layers. With niobium a temperature rise of several Kelvin is observed, which is reduced by an order of magnitude by using aluminum. A simple model satisfactorily explains this observation and predicts a 30% increase in the subgap current due to the elevated temperature. At the operating voltage for heterodyne mixing the receiver noise temperature increases by only 2%. © 1996 American Institute of Physics. [S0003-6951(96)00629-8]

In recent years superconductor–insulator–superconductor (SIS) tunnel junctions are increasingly being used up to tetrahertz frequencies. At present the highest frequency has been 1100 GHz^{1,2} which is well beyond the gap frequency of niobium indicating that the principle of operation (photon-assisted tunneling) is not intrinsically limited by the energy gap of niobium (700 GHz). However in operating at these high frequencies it has recently been observed^{3,4} that the photon-assisted tunneling step above the gap occurs at a smaller value than expected, indicating a depressed gap due to intrinsic heating by the bias current (see Fig. 2, inset). From a practical point of view this effect could result in an increased subgap current and correspondingly extra noise when irradiated. For this reason we have studied photon-assisted tunneling and heterodyne mixing up to tetrahertz frequencies using both all-niobium tunnel junctions and niobium tunnel junctions with aluminum wiring layers.

The SIS junctions considered here consist of 100-nm-thick Nb layers with an Al₂O₃ tunnel barrier in between. Fabrication details are described elsewhere.⁵ The junctions are sandwiched between leads consisting of either 200-nm-thick Nb (lower layer) and 600 nm Nb (upper layer) or of 85 nm Al with 20 nm Nb (lower layers) and 85 nm Al with 400 nm Nb in parallel (upper layers).¹

In this letter we will first analyze theoretically the heat flow and compare it with experimental results. We assume that the heat transport in the leads can be described by the following differential equation:⁶

$$-K \left(r^2 \frac{d^2 T}{dr^2} + r \frac{dT}{dr} \right) + \frac{Y}{d} r^2 (T - T_{\text{bath}}) = 0 \quad (1)$$

in which T is an equilibrium electron temperature. The heat input $I \cdot V$ is radially transported away from the junction region in two ways: by thermal conduction K in the metal films (the first term) and by heat transfer Y towards the substrate (the second term). The thickness of the leads is denoted by d , T_{bath} is the temperature of the substrate. The heating is taken to be due to the dc current only since it was

experimentally observed that varying the rf power had no influence on the junction temperature.⁷ We also assume that T is constant in a cylinder of radius $r = w/2$, where w is the width of the junction (see Fig. 1). Boundary conditions are:

$$\begin{aligned} -K \frac{dT}{dr} &= \frac{I \cdot V}{\pi w d (\cdot 2)} & \left(r = \frac{w}{2} \right) \\ T &= T_{\text{bath}} & (r \rightarrow \infty) \end{aligned}$$

The factor 2 in the denominator of the first equation accounts for the fact that the heat flow is radial in a half plane only in the top lead [see Fig. 1(a)].

The thermal conductivity K of the metal films is estimated from the measured electrical resistivities (above T_c) by the Wiedemann-Franz law. The thermal conductivity of Nb is corrected for the decrease in the quasiparticle density by a prefactor $T/1.55 \cdot \exp(-1.76 T_c/T)$ ^{8,9} (see Table I).

The thermal relaxation Y consists of a number of steps: first, the hot electrons transfer their energy to the electron bath. This process is slightly faster than the electron-phonon

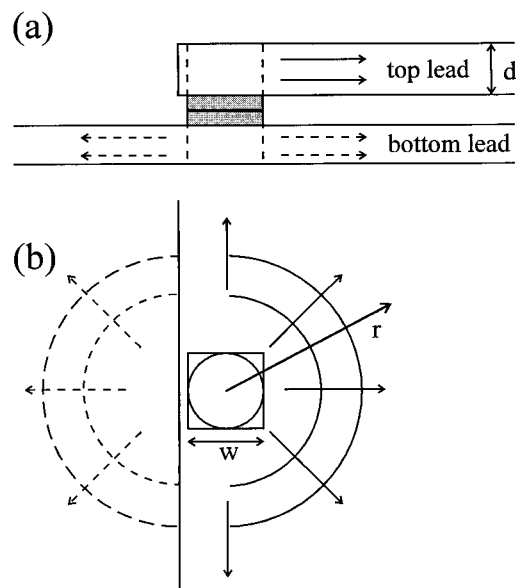


FIG. 1. Schematic layout of the junction sandwiched between the leads. (a): side view, (b): top view. The arrows indicate the radial heat flow.

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TABLE I. Material parameters for our layers needed in the calculation. The electrical conductivity σ is measured just above the transition temperature of Nb, and at 4.2 K for the Al layers. The heat conductivity K is calculated from the electrical conductivities. τ_{ee} and τ_{eph} are the electron-electron and electron-phonon relaxation times, respectively, with the latter values the electron-phonon heat transfer coefficients Y_R are calculated. These have to be compared with the Kapitza conductances Y_K ; the smaller value is used to determine the thermal healing length η .

	Nb	Al
σ (Ω m) ⁻¹	2.5×10^7 (10 K)	2×10^8 (4.2 K)
K (W/mK)	0.4	18
τ_{ee} (ns)	1	0.4
τ_{eph} (ns)	2.5	0.8
Y_R (W/m ² K)	50×10^4	20×10^4
Y_K (W/m ² K)	3×10^4	6×10^4
η (μ m)	3	5

relaxation.^{10,11} The fast electron-electron relaxation results in a well-defined equilibrium electron bath temperature, hence Eq. (1). The electron-phonon interaction causes heat transfer towards the metal phonons with coefficient $Y_R = (C_V / \tau_{eph}) \cdot d$ per unit area, with C_V the electron heat capacity, τ_{eph} the electron-phonon scattering time. We used Refs. 10 and 12 to calculate the τ_{eph} values for our Al and Nb layers respectively. The obtained values are listed in Table I. The value of the electron-phonon heat transfer coefficient has to be compared to Kapitza conductance Y_K .¹³ Since in our case $Y_K < Y_R$ at $T_{bath} = 4.2$ K the escape to the substrate is the slowest process.

With these numbers we can calculate a characteristic thermal healing length $\eta = (K \cdot d / Y_K)^{1/2}$.⁶ Since the length is much larger than the thicknesses of the various layers and the width of the junction, the assumption of a constant temperature in the cylinder as sketched in Fig. 1 is justified.

The solution of Eq. (1) is:

$$T(r) = \frac{I \cdot V \eta}{\pi K d w K_1(w/2\eta) \cdot 2} \cdot K_0\left(\frac{r}{\eta}\right) + T_{bath}. \quad (2)$$

K_0 and K_1 are modified Bessel functions of the second kind. Eq. (2) is plotted in Fig. 2 and shows the temperature distribution in the leads for both the Nb and Al case for a 15 Ω junction at 5.5 mV. Fig. 3 shows the calculated temperature at $r=0$ as a function of applied voltage. The plotted experimental values were obtained by measuring the first photon-assisted tunneling step above the gap. This step occurs at $V = (2\Delta + \hbar\omega)/e$. If at that voltage the temperature is higher the value of Δ is associated with the elevated temperature. This value is lower than the value of Δ at $V = 2\Delta/e$ and therefore the photon step occurs at a lower voltage. This voltage is measured for different frequencies to derive the gap value and the temperature using the BCS gap equation. The temperatures obtained from samples with different resistances are scaled to values corresponding to 15 Ω junctions. The difference between the use of Nb and Al leads is clear, from experiment as well as from the calculated values.

To investigate the influence of the elevated temperature on the mixer performance, we calculated the subgap current increase at the operating voltage for heterodyne detection with the standard tunneling equation.¹⁴ This approach is

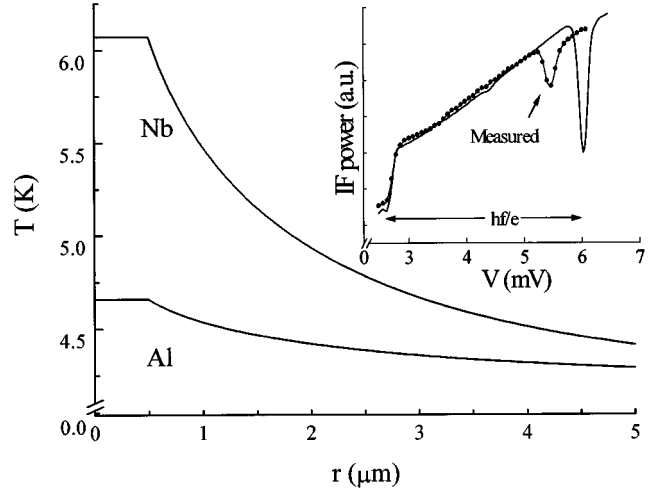


FIG. 2. Calculated temperature profiles using Nb and Al leads. The insert shows (dots: experiment, full line: theory) the power output in a heterodyne experiment, which is proportional to the differential resistance of the SIS junction. The apparent lower gap is indicative of intrinsic heating.

valid for high quality junctions in the temperature range of interest.¹⁵ The dc power input results in an increased temperature of 0.05 K for a junction with Al leads and 0.39 K for Nb leads. The subgap current increase is 30% and 7% for Nb and Al leads, respectively. To estimate the influence on the mixer noise temperature T_M we use the 3-port Tucker theory.¹⁶ The increase in T_M at 460 GHz is 2% for the Nb case and 0.4% for a junction with Al striplines as compared to a junction without heating effects. Previously measured values¹⁷ at the same frequency for an all-Nb junction suggested an increase of 4% and 1% for corresponding temperature increases. From these values we can conclude that the temperature increase during operation has little influence on the performance of the Nb SIS mixer.

A most promising result for the development of small area high current density THz mixers is that the area of the junction has little influence on the heating since the prefactor in Eq. (2) is weakly dependent of the junction area w^2 , be-

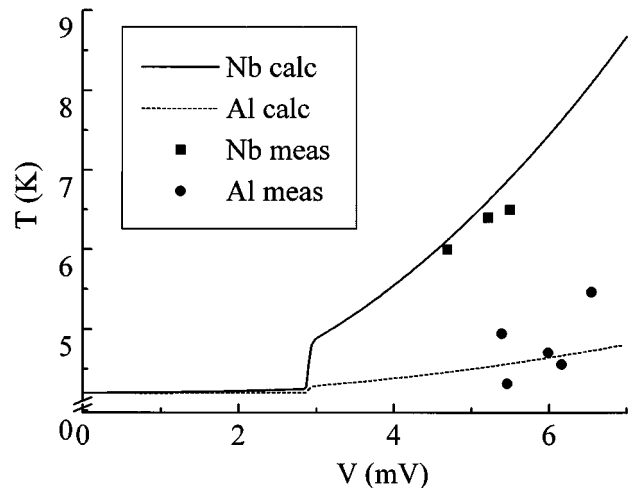


FIG. 3. Temperature as function of applied voltage for a 15 Ω junction. Calculated temperatures are presented as lines, measured temperatures are shown by dots.

cause the $K_1(w/2\eta)$ term in Eq. (2) nearly cancels the w term. Therefore the current density can be raised without severe additional heating effects if the total resistance is kept constant by decreasing the junction size. The only parameters effectively involved are the embedding configuration and the thermal conductivity of the embedding material. An example of the first is the use of junctions in series,⁴; the thermal healing length is usually comparable to the spacing of the junctions, leading to a large effect on the temperature. An example of the effect of the thermal conductivity is the application of NbN as superconductor in SIS junctions and as low-loss stripline material. In using NbN junctions severe heating effects have been observed,¹⁷ which is partially due to the low thermal conductivity and partially due to the larger gap voltage of NbN, which requires a higher bias voltage. In both cases application of thick layers of clean highly conducting metals in an appropriate configuration is expected to improve the performance of the devices considerably.

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